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**AUG 04 2005**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:  
**DALE E. GULICK**

Serial No.: 10/066,879

Filed: February 4, 2002

For: ASF DETERMINATION USING  
CHIPSET-RESIDENT WATCHDOG  
TIMER

Group Art Unit: 2114

Examiner: M. Duncan

Atty. Dkt. No.: 2000.051700

**DECLARATION UNDER 37 C.F.R. § 1.131 OF DALE E. GULICK**

1. My name is Dale E. Gulick. I have personal knowledge of the facts stated herein.
2. I am currently employed with Advanced Micro Devices, Inc. in Austin, Texas. I am a named inventor on application Serial No. 10/066,879 entitled "ASF Determination Using Chipset Resident Watchdog Timer."
3. Attached as Exhibit A is a copy of the invention disclosure form I prepared in Austin, Texas for the invention described in the above-referenced patent application. I prepared and signed the invention disclosure form on January 28, 2000, as indicated by the date adjacent my signature.
4. The attached invention disclosure form was provided with internal tracking number TT4031 by AMD's legal department, and it was sent to the law firm of Williams, Morgan & Amerson on or about May 12, 2000, with a request to prepare a United States patent application for the invention disclosed in the invention form.

5. From about March 9, 2000 to about May 2, 2001, I received drafts of 16 patent applications related to patent application Serial No. 10/066,879. I diligently reviewed these patent applications.

6. I executed the formal papers for the application Serial No. 10/066,879 on January 30, 2002.

7. I understand that willful false statements and the like so made are punishable by fine or imprisonment, or both, and may jeopardize the validity of the application or any patent issuing thereon.

8. I declare under penalty of perjury that the foregoing is true and correct.

Date

8/3/05

Dale E. Gulick

